(12) INTERNATIONAL APACATION PUBLISHED UNDER THE PATENT COOLERATION TREATY (PCT)

CORRECTED VERSION

(19) World Intellectual Property Organization

International Bureau





(43) International Publication Date 15 January 2004 (15.01.2004)

PCT

(10) International Publication Number WO 2004/006451 A1

(51) International Patent Classification7: H04B 1/16, 1/40

(21) International Application Number:

PCT/FI2003/000530

(22) International Filing Date: 1 July 2003 (01.07.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data: 20021314

3 July 2002 (03.07.2002) FI

(71) Applicant (for all designated States except US): NOKIA CORPORATION [FI/FI]; Keilalahdentie 4, FIN-02150 Espoo (FI).

(72) Inventors; and

- (75) Inventors/Applicants (for US only): VIERO, Timo [FI/FI]; Juvanpuisto 10, FIN-02920 Espoo (FI). KIISKI, Matti [FI/FI]; Mustalinnuntie 21, FIN-90460 Oulunsalo (FI).
- (74) Agent: KOLSTER OY AB; Iso Roobertinkatu 23, P.O. Box 148, FIN-00121 Helsinki (FI).
- (81) Designated States (national): AE, AG, AL, AM, AT (utility model), AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ (utility model), CZ, DE (utility model), DE, DK (utility model), DK, DM, DZ, EC, EE (utility model), EE, ES, FI (utility model), FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN,

MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK (utility model), SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

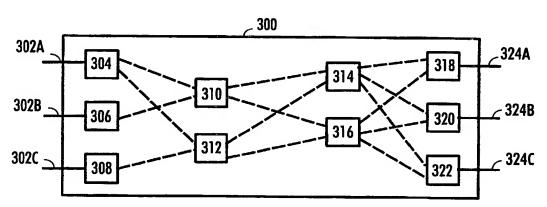
(84) Designated States (regional): ARIPO utility model (GH), ARIPO patent (GH), ARIPO utility model (GM), ARIPO patent (GM), ARIPO utility model (KE), ARIPO patent (KE), ARIPO utility model (LS), ARIPO patent (LS), ARIPO utility model (MW), ARIPO patent (MW), ARIPO utility model (MZ), ARIPO patent (MZ), ARIPO utility model (SD), ARIPO patent (SD), ARIPO utility model (SL), ARIPO patent (SL), ARIPO utility model (SZ), ARIPO patent (SZ), ARIPO utility model (TZ), ARIPO patent (TZ), ARIPO utility model (UG), ARIPO patent (UG), ARIPO utility model (ZM), ARIPO patent (ZM), ARIPO utility model (ZW), ARIPO patent (ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,

[Continued on next page]

(54) Title: DATA TRANSMISSION METHOD AND ARRANGEMENT



(57) Abstract: A circuit arrangement for signal processing in a receiver and/or transmitter of a radio system, the circuit arrangement nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) are arranged to execute at least one operation, the circuit arrangement comprising means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing at least one of the signals or signal components according to the one or more predetermined division criteria for signal classes, the circuit arrangement comprising means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) for executing predetermined operations signal-classwise.

CATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT) (12) INTERNATIONAL APA

(19) World Intellectual Property Organization

International Bureau



(43) International Publication Date 15 January 2004 (15.01.2004)

PCT

(10) International Publication Number WO 2004/006451 A1

- (51) International Patent Classification⁷: H04B 1/16, 1/40
- (21) International Application Number:

PCT/FI2003/000530

(22) International Filing Date: 1 July 2003 (01.07.2003)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

20021314

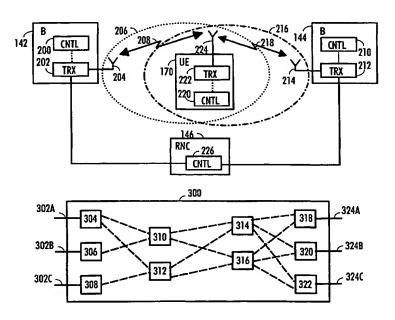
3 July 2002 (03.07.2002)

- (71) Applicant (for all designated States except US): NOKIA CORPORATION [FI/FI]; Keilalahdentie 4, FIN-02150 Espoo (FI).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): VIERO, Timo [FI/FI]; Juvanpuisto 10, FIN-02920 Espoo (FI). KIISKI, Matti [FI/FI]; Mustalinnuntie 21, FIN-90460 Oulunsalo (FI).
- (74) Agent: KOLSTER OY AB; Iso Roobertinkatu 23, P.O. Box 148, FIN-00121 Helsinki (FI).

- (81) Designated States (national): AE, AG, AL, AM, AT (utility model), AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ (utility model), CZ, DE (utility model), DE, DK (utility model), DK, DM, DZ, EC, EE (utility model), EE, ES, FI (utility model), FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK (utility model), SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO utility model (GH), ARIPO patent (GH), ARIPO utility model (GM), ARIPO patent (GM), ARIPO utility model (KE), ARIPO patent (KE), ARIPO utility model (LS), ARIPO patent (LS), ARIPO utility model (MW), ARIPO patent (MW), ARIPO utility model (MZ), ARIPO patent (MZ), ARIPO utility model (SD), ARIPO patent (SD), ARIPO utility model (SL), ARIPO patent (SL), ARIPO utility model (SZ), ARIPO patent (SZ), ARIPO utility model (TZ), ARIPO patent (TZ), ARIPO utility model (UG), ARIPO patent (UG), ARIPO utility model (ZM), ARIPO patent (ZM), ARIPO utility model (ZW), ARIPO patent (ZW), Eurasian

[Continued on next page]

(54) Title: DATA TRANSMISSION METHOD AND ARRANGEMENT



(57) Abstract: A circuit arrangement for signal processing in a receiver and/or transmitter of a radio system, the circuit arrangement nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) are arranged to execute at least one operation, the circuit arrangement comprising means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing at least one of the signals or signal components according to the one or more predetermined division criteria for signal classes, the circuit arrangement comprising means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) for executing predetermined operations signal-classwise.



patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN,

TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

of inventorship (Rule 4.17(iv)) for US only

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

DATA TRANSMISSION METHOD AND ARRANGEMENT

FIELD

10

20

25

30

[0001] The invention relates to a method and a circuit arrangement for signal processing in a receiver and/or transmitter of a radio system.

5 BACKGROUND

[0002] Future radio systems will transmit signals of different types simultaneously and even on the same carrier wave. The signals may be, for instance, encrypted, modulated, power limited in various ways or they may belong to different quality classes. It is possible to send simultaneously speech, data and images, which are all processed both in the transmitter and in the receiver in various manners. A plurality of different radio system standards can be in simultaneous use, for instance, data packets, video image and speech each having one. A problem arises how a system, in which signals are processed in different manners, can be implemented in practice.

15 BRIEF DESCRIPTION

[0003] The object of the invention is to provide an improved method and an improved circuit arrangement. One aspect of the invention is a method for processing signals in a radio system receiver and/or transmitter. The method comprises determining for different circuit arrangement nodes at least one operation to execute, determining one or more division criteria for signal classes, dividing at least one of the signals or signal components according to the one or more division criteria for signal classes, executing the predetermined operations in the circuit arrangement nodes signal-classwise.

[0004] One aspect of the invention is a method for processing signals in a radio system receiver and/or transmitter. The method comprises determining for different circuit arrangement nodes at least one operation to execute and selecting a modification level from the circuit arrangement, merging the selected modification level nodes and deleting irrelevant nodes and links between the nodes and/or adding new links, determining one or more division criteria for signal classes for dividing the signals or signal components, dividing at least one of the signals or signal components according to the one or more division criteria for signal classes, executing the predetermined operations in the circuit arrangement nodes signal-classwise.

10

15

20

25

30

[0005] One aspect of the invention is a circuit arrangement for signal processing in a radio system receiver and/or transmitter. Nodes of the circuit arrangement are arranged to execute at least one operation, the circuit arrangement comprising means for dividing at least one of the signals or signal components according to the one or more predetermined division criteria for signal classes, the circuit arrangement comprising means for executing the predetermined operations signal-classwise.

[0006] The preferred embodiments of the invention are disclosed in the dependent claims.

[0007] The invention is based on the idea that signals or signal components are classified, according to need, into different signal classes and various operations are executed signal-classwise. Division criteria for signal classes include, for instance, a carrier wave, a modulation method or a need for power limitation.

[0008] An example of the advantages of the invention is that signal processing can be enhanced, in particular, in systems, which employ a plurality of radio standards, or in which signals have to be processed in various manners for other reasons.

LIST OF THE DRAWINGS

[0009] In the following, the preferred embodiments of the invention will be described by way of example with reference to the attached drawings, wherein

Figure 1 is a simplified block diagram of the structure of a radio system;

Figure 2 is a simplified block diagram of the structure of WCDMA radio system;

Figure 3 is an example of a base station depicted by means of nodes;

Figure 4 is a flow chart;

Figure 5 is an example of tree architecture;

Figure 6 is an example of loop architecture;

Figures 7A to 7B are examples of modification of the architectural structure.

10

15

20

25

30

35

DESCRIPTION OF THE EMBODIMENTS

[0010] Since the radio systems of the second and the third generations and their various hybrids, i.e. radio systems of the 2.5 generation, are in use worldwide and under constant development, in Figure 1 the embodiments are described in a simplified radio system, which comprises network elements of different generations in parallel. In the description, the radio system of the second generation is represented by the GSM (Global System for Mobile Communications), the third generation radio system by a radio system which is based on the GSM, uses the EDGE technique (Enhanced Data Rates for Global Evolution) for increasing the data transmission rate, and can also be used for implementing packet transmission in the GPRS system (General Packet Radio System). Sometimes the EDGE system is seen as a 2.5 generation system. The third generation radio system is also represented by a radio system which is known at least by the names IMT-2000 (International Mobile Telecommunications 2000) and UMTS (Universal Mobile Telecommunications System). The embodiments are not, however, restricted to these systems given as examples but a person skilled in the art may apply the solution in other radio systems provided with the necessary properties.

[0011] Figure 1 is a simplified block diagram, which describes the most important network elements of the radio system and the interfaces between them. The structure and function of the network elements are not described in detail because they are commonly known.

[0012] The main parts of the radio system are a core network (CN) 100, a radio access network 130 and user equipment (UE) 170. The term UTRAN is an abbreviation from UMTS Terrestrial Radio Access Network, i.e. the radio access network belongs to the third generation and is implemented by wideband code division multiple access WCDMA. Figure 1 further illustrates a base station system 160, which belongs to the 2/2.5 generation and is implemented by time division multiple access TDMA.

[0013] Generally, the radio system can also be defined as follows: the radio system consists of a user terminal, which is also called a subscriber terminal or a mobile station, and of a network part, which includes the fixed infrastructure of the radio system, i.e. a core network, a radio access network and a base station system.

[0014] The structure of the core network 100 corresponds to a combined structure of the GSM and GPRS systems. The network elements of

10

15

20

25

30

35

the GSM are responsible for implementing circuit-switched connections and the network elements of the GPRS are responsible for implementing packetswitched connections. However, some of the network elements are included in both systems.

[0015] A mobile services switching centre (MSC) 102 is the centre of the circuit-switched side of the core network 100. The same mobile services switching centre 102 can be used to serve the connections of both the radio access network 130 and the base station system 160. The tasks of the mobile services switching centre 102 typically include switching, paging, user terminal location registration, handover management, collection of subscriber billing information, data encryption parameter management, frequency allocation management and echo cancellation.

[0016] The number of mobile services switching centres 102 may vary: a small network operator may have only one mobile services switching centre 102, whereas large core networks 100 may have several ones. Figure 1 shows another mobile services switching centre 106 but its connections to other network elements are not illustrated to keep Figure 1 sufficiently clear.

[0017] Large core networks 100 may comprise a separate gateway mobile services switching centre (GMSC) 110, which is responsible for circuit-switched connections between the core network 100 and the external networks 180. The gateway mobile services switching centre 110 is located between the mobile services switching centres 102, 106 and the external networks 180. The external network 180 may be, for example, a public land mobile network PLMN or a public switched telephone network PSTN.

[0018] The core network 100 typically comprises other parts, too, such as a home location register HLR, which includes a permanent subscriber register and, if the radio system supports the GPRS, a PDP address (PDP = Packet Data Protocol), and a visitor location register VLR, which includes information on roaming of the user terminals 170 in the area of the mobile services switching centre 102. Not all parts of the core network are shown in Figure 1 to keep it clear.

[0019] A serving GPRS support node (SGSN) 118 is the centre of the packet-switched side of the core network 100. The main task of the serving GPRS support node 118 is to transmit and receive packets with the user terminal 170 supporting packet-switched transmission, utilizing the radio access network 130 or the base station system 160. The serving GPRS support node

10

15

20

25

30

35

118 includes subscriber information and location information on the user terminal 170.

[0020] A gateway GPRS support node (GGSN) 120 on the packet-switched side corresponds to the gateway mobile services switching centre 110 on the circuit-switched side, with the exception that the gateway GPRS support node 120 has to be able to route outgoing traffic from the core network 100 to external networks 182, whereas the gateway mobile services switching centre 110 typically routes only the incoming traffic. In the example, the external networks 182 are represented by the Internet, via which a considerable part of wireless telephone traffic can be transmitted in the future.

[0021] The base station system 160 consists of a base station controller (BSC) 166 and base transceiver stations (BTS) 162, 164. The base station controller 166 controls the base transceiver stations 162, 164. In principle, the devices implementing the radio path and their functions should be located in the base transceiver station 162, 164 and the management devices in the base station controller 166. The implementation may naturally deviate from this principle.

[0022] The base station controller 166 is usually responsible for the following tasks, for example: management of the radio resources of the base transceiver station 162, 164, intercell handover, frequency management, i.e. allocation of frequencies to the base transceiver stations 162, 164, management of frequency hopping sequences, measurement of time delays on the uplink, implementation of the operation and maintenance interface, and management of power control.

[0023] The base station 162, 164 includes at least one transceiver which forms one carrier wave. In the GSM systems, one carrier wave usually comprises eight time slots, i.e. eight physical channels. One base station 162, 164 may serve one cell or several sectorized cells. The cell diameter may vary from a few metres to dozens of kilometres. The base station 162, 164 is often deemed to include a transcoder, too, for performing conversion between the speech coding format used in the radio system and the speech coding format used in the public switched telephone system. In practice the transcoder, however, is usually physically located in the mobile services switching centre 102. The base station 162, 164 is usually responsible for the following tasks, for example: calculation of a TA (timing advance), measurements on the uplink, channel coding, encryption, decryption and frequency

hopping.

5

10

15

20

25

30

35

[0024] The radio access network 130 consists of radio network subsystems 140, 150. Each radio network subsystem 140, 150 consists of radio network controllers (RNC) 146, 156 and B nodes 142, 144, 152, 154. The B node is rather an abstract concept, which is frequently replaced by the term 'base station'.

[0025] Functionality the radio network controller 140, 150 roughly corresponds to the base station controller 166 of the GSM system and the B node 142, 144, 152, 154 to the base station 162, 164 of the GSM system. Solutions are also available where the same device serves both as the base station and as the B node, i.e. the device can simultaneously implement the TDMA and the WCDMA radio interface.

[0026] The user terminal 170 consists of two parts: mobile equipment (ME) 172 and a UMTS subscriber identity module (USIM) 174. In the GSM system the identity module of the system is naturally used. The user terminal 170 comprises at least one transceiver for establishing a radio connection to the radio access network 130 or to the base station system 160. The user terminal 170 may include at least two different subscriber identity modules. In addition, the user terminal 170 comprises an antenna, a user interface and a battery. Nowadays various kinds of user terminals 170 are available, e.g. vehicle-mounted and portable terminals. The user terminals 170 also have properties similar to those of a personal computer or a portable computer.

[0027] The USIM 174 includes information on the user and information on data security, e.g. an encryption algorithm, in particular.

[0028] In the following, the interfaces between different network elements shown in Figure 1 are presented in Table 1. It is obvious to a person skilled in the art that the interfaces included in the radio telecommunications system are determined by the hardware implementation and the standard used, for which reason the interfaces of the system may differ from those shown in Figure 1. In the UMTS, the most important interfaces are the lu interface between the core network and the radio access network, which is divided into the luCS (CS = Circuit Switched) interface of the circuit-switched side and the luPS (PS = Packet Switched) interface of the packet-switched side, and the Uu interface between the radio access network and the user terminal. In the GSM, the most important interfaces are the A interface between the base station controller and the mobile services switching centre, the Gb interface

7

between the base station controller and the serving GPRS support node, and the Um interface between the base station and the user terminal. The interface defines what kind of messages different network elements may use to communicate with one another. The object of the standardisation of interfaces is to enable function between network elements of different manufacturers. In practice, however, some of the interfaces are manufacturer-specific.

| Interface | Between network elements |
|-----------|--------------------------|
| Uu | UE-UTRAN |
| lu | UTRAN-CN |
| luCS | UTRAN-MSC |
| luPS | UTRAN-SGSN |
| Cu | ME-USIM |
| lur | RNC-RNC |
| lub | RNC-B |
| Α | BSS-MSC |
| Gb | BSC-SGSN |
| A-bis | BSC-BTS |
| Um | BTS-UE |
| E | MSC-MSC |
| Gs | MSC-SGSN |
| PSTN | MSC-GMSC |
| PSTN | GMSC-PLMN/PSTN |
| Gn | SGSN-GGSN |
| Gi | GGSN-INTERNET |

Table 1

10

15

[0029] In the following, a cellular WCDMA radio telecommunications system will be described by means of Figure 2. Figure 2 shows part of a simplified radio system, which comprises a subscriber terminal 170, two base stations 142, 144 and a base station controller 146. The first base station 142 comprises a transceiver 202, an antenna 204 and a control block 200. Likewise, the second base station 144 comprises a transceiver 212, an antenna 214 and a control block 210. The base station controller 146 also comprises a

10

15

20

25

30

35

control block 226. The user terminal 170 also comprises a normal transceiver 222 and an antenna for implementing a radio link and a control block 220. The transceivers 202, 212, 222 use the CDMA technology (Code Division Multiple Access). In the CDMA technology, i.e. in code division multiple access, radio resources are allocated to each user by means of user-specific codes. The technique is generally known, for which reason it will not be described in greater detail here. The antennas 204, 214, 224 can be implemented by common prior art solutions, e.g. as omnidirectional antennas or as antennas employing a directed antenna beam.

[0030] In the radio telecommunications system, the radio cells created by base stations usually overlap to some extent to provide extensive coverage. This is illustrated in Figure 2 by a radio cell 206 created by the base station 142 and a radio cell 216 created by the base station 144. In the existing radio telecommunications systems, wireless telecommunications connections are established by user terminals and base stations which communicate with one another on a radio connection, i.e. calls or data transmission connections between different user terminals are established via base stations. This is illustrated in Figure 2 by radio connections 208, 218. In particular, Figure 2 shows a situation where a user terminal 170, which may be mobile, communicates over a radio connection with the first base station 142, simultaneously measuring common pilots of this base station 142 and the second base station 144 for a possible handover. In a typical situation the radio connection of the user terminal shifts to the carrier wave of the second base station when there is free capacity in the new cell and the new connection has a better quality. Channel and cell handovers enable continuity of the radio connection when the user terminal moves or the physical radio channel changes as a function of time.

[0031] The control blocks 200, 210, 220, 226 refer to a block which controls the operation of the device and which is nowadays usually implemented as a processor and its software, but various hardware solutions are also feasible, e.g. a circuit built from separate logic components or one or more application specific integrated circuits ASIC. A hybrid of these different implementations is also feasible. When selecting the implementation method, a person skilled in the art will consider the requirements set on the size and power consumption of the device, the necessary processing capacity, the production costs and the production volumes.

10

15

20

25

30

35

9

[0032] Further information on radio telecommunications systems is available in the literature and standards of the field.

[0033] With reference to Figure 3, an example of a base station will be described by means of nodes. The base station 300 comprises input ports 302A, 302B, 302C, through which the base stations receives signals from other network elements, such as a radio network controller RNC, a serving GPRS support node SGSN or a gateway GPRS support node GGSN. The base station also comprises output ports 324A, 324B, 324C, through which signals are applied to an antenna unit to be transmitted on a radio path. The data to be transmitted to the base station can also be in the packet form. The data packets received by the base station are digital and baseband packets. The data packets typically include an address part and an actual payload part. There are several standards for packet-switched data, and they are not described in detail herein.

[0034] In the example of Figure 3, the base station is assumed to consist of nodes 304 to 322 and links between them, which links are indicated by broken lines. The links between nodes can be either point-to-point links or buses. In practice, the bus structure is more common. The nodes can be divided into nodes that perform baseband processing and nodes that perform radio-frequency processing. The nodes can also be of some other type.

[0035] The nodes can be implemented as an application-specific integrated circuit (ASIC), a field programmable gate array (FPGA) or as combinations thereof.

[0036] The task of the base station is to transmit data onto a radio path and thereby to a subscriber terminal of the recipient. Hence, the data must be subjected to modification operations required by the radio interface standard, such as modulation and upmixing onto a radio frequency. The base station also receives from the subscriber terminals carrier-frequency data in the analogue format. Various modification operations, such as downmixing onto a baseband, are performed on the data received by the base station. The operations are typically executed in the nodes, which can also be called processing units, for instance.

[0037] Next will be described a method for signal processing in a radio system receiver and/or transmitter. To describe the method, the base station is assumed to consist of nodes, i.e. processing units, and links between them. The method processes either signals or signal components. The signal

10

15

20

25

30

35

component refers, for instance, to an I-component or a Q-component of an I/Q modulated signal or a part of a summed signal.

[0038] The method starts in block 400. In block 402, at least one operation to be executed is determined for different nodes of the circuit arrangement. The nodes are typically divided into baseband nodes or radio-frequency nodes depending on the tasks assigned to the nodes: for instance, signal power amplification is performed in the radio-frequency parts, whereas signal frequency change, i.e. downmixing or upmixing, is performed in the baseband parts. The number of nodes, their tasks and links between nodes vary depending on the application. There are various solutions for the basic architecture, which include various tree and loop models. The tree model is a linked graph, which is not a circle. As the name indicates, the loop model comprises a loop, i.e. a ring, and in many cases it can also be regarded as a coupled tree model.

[0039] An advantageous architectural configuration applicable to the implementation of the invention is a combined tree structure. Naturally, the combined tree structure can be implemented in a variety of ways. An example of this structure is shown in Figure 5. Nodes 502 to 508 constitute a first subtree and nodes 510 to 516 a second subtree. The number of subtrees may vary. Both subtrees are connected to a so-called zero node or root node 500. In a base station that is implemented by means of the combined subtree structure one branch often carries out transmitter tasks and the other branch, in turn, carries out receiver tasks. Nodes 504, 514, 506, 512 are data transfer nodes, through which desired information can be conveyed from one tree branch to the other, for instance information between the transmitter and the receiver, via links 534, 536. Other links between nodes are indicated by lines 518, 520, 522, 524, 526, 528, 530, 532.

[0040] Another architectural configuration described in greater detail herein that is applicable to the implementation of the invention is a loop structure. The loop structures can be implemented in a variety of ways. An example of this structure is shown in Figure 6. In a base station that is implemented by means of the centralized loop structure, at least two subtrees are typically connected to the loop, of which subtrees one carries out tasks of the radio-frequency parts and the other carries out tasks of the baseband parts. Thus, nodes 600, 606, for instance, carry out the tasks of the radio-frequency parts and nodes 608, 614 carry out the tasks of the baseband parts. Nodes 602,

10

15

20

25

30

35

604, 610, 612 are data transfer nodes and they take care of data transfer from one node to another. Links between nodes are indicated by lines 616, 618, 620, 622, 624, 626, 628.

[0041] A circuit arrangement can be implemented in a variety of ways, in addition to the tree model or the loop model, for instance by combining these two models in a suitable manner.

[0042] In a second preferred embodiment of the invention, the circuit arrangement is provided by modifying a simple architectural configuration, for instance, the tree model in a manner that enables the best-suited circuit arrangement, i.e. architectural, solution for each particular application. In the method from the circuit arrangement there is selected a modification level that is typically a sublevel. The selected modification level nodes are merged, whereafter irrelevant nodes and links are deleted between the nodes and/or new links are added. In this manner it is also possible to implement the above-described combined tree structure and loop structure.

[0043] By means of Figures 7A to 7B the modification will be described in greater detail. Figure 7A shows an example of a simple tree structure. Node 700 is a root node, nodes 706, 708 are first-level subnodes, nodes 714, 716 are second-level subnodes and nodes 726, 728, 730, 732 are third-level subnodes. The links between different nodes are indicated by lines 702, 704, 710, 712, 718, 720, 722, 724. The tree may also branch from the subnodes, as it can be seen from Figure 7A: on the third level there are two nodes in each branch of the tree. The tree model is to be modified and a decision is made to select the third level as the modification level. An example of a modified model is shown in Figure 7B. The selected modification level nodes are connected in pairs 726 and 730 as well as 728 and 732, whereafter unnecessary nodes and links are deleted between the nodes. Thus, nodes 714, 716 and links, i.e. connections 718, 720, 722, 724 are deleted. In modification it is also possible to create new links between the nodes. In Figure 7B, 734 and 736 represent new links.

[0044] In block 404, one or more division criteria for signal classes are determined for dividing the signals or the signal components. The signal classes can still be divided into subclasses. Thus the signal classes constitute a hierarchic signal class system, which comprises several levels. The basic principle of the method is the same irrespective of whether the signal classes have subclasses or not.

[0045] A division criterion for signal classes may be, for instance, a transmission or reception antenna beam, i.e. the signals transmitted from or received in the same antenna beam are determined to belong to the same signal class, or a subcarrier of the OFDM (Orthogonal Frequency Division Multiplexing) system, i.e. the signals intended for the same subcarrier are determined to belong to the same signal class. Other signal class division criteria include, for instance, a spreading ratio or a spreading code, a modulation method, a service quality class, cutting of power level, a standard used at a radio interface or a carrier. Division criteria for signal classes may also be the following: signals with different modulation accuracy requirements are divided into different signal classes after spatial, temporal and/or frequency-level pre-processing, or signals are divided into different signal classes after interference cancellation pre-processing.

[0046] Requirements for modulation accuracy refer to various parameters representing the quality of the radio-frequency operation of the system, such as EVM (Error Vector Magnitude), which refers to a vector representing distance between a noisefree symbol location and the actual symbol location, when interference is summed in the modulated signal. The error vector is one prior art criterion for the quality of modulation. Another parameter representing the requirements for modulation accuracy is the maximum value of a peak code domain error, which is used in the WCDMA systems and which represents an error due to modulation inaccuracy in the despread signal. Other optional parameters include a frequency error and a phase error, for instance.

[0047] Distribution of signals to different antenna beams in beam forming can be given as an example of spatial preprocessing. Temporal preprocessing is, for instance, channel equalization, which can be implemented by means of signal classes for instance as follows: radio resource users are divided into signal classes on the basis of the data speeds of the links and the channel equalization is performed only on the signals of the highest data speed. In that case resources are saved, because the channel equalization is only performed on the signals that benefit most thereof. Preprocessing that cancels interference can be implemented signal-classwise for instance as follows: the signal that interferes worst is detected first, it is modulated and subtracted from the received signal, next is detected the signal that interferes next worst and so on and so forth, until all the desired signals are detected.

10

15

20

25

30

35

[0048] Division criteria for signal classes may be one or more class-specifically, and if necessary, the division criteria may also be altered. The division criteria for signal classes vary application-specifically.

[0049] In block 406, at least one of the signals or the signal components is divided according to one or more division criteria for signal classes. The following is an example of using a modulation method as a division criterion: in HSDPA (High Speed Downlink Packet Access) systems baseband nodes forward signals intended for the same antenna and the same carrier divided into two signal classes, of which the QPSK (Quadrature Phase-Shift Keying) modulated signals are in one class and the 16QAM or 64QAM modulated signals are in the other class. So the signals in different signal classes can be modulated separately. Optionally, the signal is despread and the next operation, for instance cutting of power level, is performed on the sum signal. The cutting of power level refers in this connection to a method, by which the relation of the signal peak power and the average power is reduced and/or the peak power is lowered.

[0050] Apart from classes, the signals can also be divided into subclasses, i.e. for instance such that first the signals are divided according to the modulation method, whereafter the modulation classes are divided into subclasses on the basis of the service quality class, for instance. Thus the signal classes constitute a hierarchic signal class system which comprises a plurality of levels.

[0051] In block 408, predetermined operations are performed in the nodes of the circuit arrangement signal-classwise. The operations typically include various tasks of radio frequency parts or baseband parts of a radio transmitter or receiver, such as frequency change, modulation and power level adjustment of the signal.

[0052] Division of signals into different classes can also be utilized in monitoring the network traffic load. For instance, if the transmission power exceeds a predetermined threshold value determined for a signal class, the power level of the signals in that class is lowered or the signals are not transmitted until the traffic load on the radio path reduces sufficiently. In reception, in turn, the power of the received signal can be determined signal-classwise.

[0053] Because signal processing may affect the quality of the signals, at least in some cases feedback information is needed for process control. Thus it is often advantageous if the circuit arrangement enables feedback

10

15

20

25

30

35

information transfer, i.e. data transfer links between desired nodes are bidirectional. The bidirectional links can also be utilized in the above-described monitoring of network traffic load.

[0054] Each inter-node link has a maximum transfer capacity, within which the number and type of the signal classes to be transferred can be altered. Different signal classes may comprise signals that are intended for the same carrier, i.e. the different classes may comprise signals intended for the same antenna, or the different classes include signals intended for different antennas.

[0055] The nodes may unicast or multicast. The node transmission mode can be changed by means of control. Particularly in the uplink, transferring signal classes from one radio-frequency node to a plurality of baseband nodes, i.e. copying, is advantageous in many cases when a broadband radio system is concerned. The copying operation is typically repeated in the course of the transfer, i.e. in packet transmission, for instance, several consecutive packets are copied and transferred to a plurality of nodes of the next level. When data is transmitted to a plurality of nodes of the next level, the frequently applied term is multicast transmission. When data is transmitted from the node to one or more nodes of the next level, the frequently applied term is broadcast transmission. If data is transferred to only one node of the next level, the term used is unicast transmission.

[0056] The method ends in node 410. Arrow 412 indicates the repeatability of the method, for instance, such that signals are first classified in one manner for a particular operation, whereafter the signals are classified for another operation either in the same manner or in a different manner, depending on the operation. It should be noted that when the method is applied, all the signals need not necessarily be classified, but the classified signals are selected according to each particular operation, and thus the number of signals to be classified varies.

[0057] Next, signal processing will be described in greater detail signal-classwise, using a signal summing operation as an example. A signal class is formed, for instance, of signals that are intended for the same antenna and the same carrier. In summing the signals in the downlink, the baseband nodes form sum signals of similar signal classes. In the node the signal classes of the same type are summed and the node output is one sum signal class, not a plurality of individual signal classes.

10

15

20

25

30

35

[0058] Next will be described in greater detail two examples of a circuit arrangement, to which the method of the invention can be applied. One exemplary circuit arrangement is at least substantially in accordance with the combined tree structure such that at least one tree branch performs transmitter tasks and at least one other branch performs receiver tasks, and in which circuit arrangement one or more nodes of different branches is connected in a predetermined manner. Naturally, the combined tree structure can be implemented in a variety of ways. One example of this structure is shown in Figure 5. Nodes 502 to 508 form a first subtree and nodes 510 to 516 a second subtree. Both subtrees are connected to a so-called zero node 500. In a base station, which is implemented by means of the structure shown in the figure, typically one tree branch performs the tasks of a transmitter and the second branch performs the tasks of a receiver. Nodes 504, 514, 506, 512 are data transfer nodes, through which desired information can be conveyed from one tree branch to the other, for instance, information between the transmitter and the receiver, via links 534, 536. Other links between nodes have been indicated by reference numerals 518, 520, 522, 524, 526, 528, 530, 532.

[0059] The circuit arrangement also comprises a control unit 534, which controls the division into signal classes. If a base station is concerned, the control unit may be located in the base station, a base station controller or a radio network controller. The control unit is typically implemented by means of software, for instance, as an ASIC or a similar solution.

[0060] Another advantageous architectural structure that can be applied in the implementation of the invention is a loop structure. The loop structures can be implemented in a variety of ways. One example of this structure is shown in Figure 6. In the base station, which is implemented in the manner shown in Figure 6, at least two subtrees are typically connected to the loop, of which subtrees one performs the tasks of the radio-frequency parts and the other performs the tasks of the baseband parts. Thus, for instance, nodes 600, 606 perform the tasks of the radio-frequency parts and nodes 608, 614 perform the tasks of the baseband parts. Nodes 602, 604, 610, 612 are data transfer nodes and they are responsible for data transfer from one node to the other. Inter-node links are indicated by reference numerals 616, 618, 620, 622, 624, 626, 628.

[0061] The circuit arrangement also comprises a control unit 534, which controls the division into signal classes. If a base station is concerned,

10

the control unit may be located in the base station, a base station controller or a radio network controller. The control unit is typically implemented by means of software, for instance, as an ASIC or a similar solution.

[0062] Apart from the tree model or the loop model, the circuit arrangement can be implemented in a variety of ways, for instance, by combining these two methods in a suitable manner.

[0063] Even though the invention is described in the above with reference to the example of the accompanying drawings, it is apparent that the invention is not restricted thereto but it can be modified in a variety of ways within the scope of the inventive idea disclosed in the attached claims.

CLAIMS

5

10

15

20

25

1. A method for signal processing in a receiver and/or transmitter of a radio system, **c h a r a c t e r i z e d** by comprising:

determining (402) for different circuit arrangement nodes at least one operation to execute,

determining (404) one or more division criteria for signal classes for dividing signals or signal components,

dividing (406) at least one of the signals or signal components according to the one or more division criteria for signal classes,

executing (408) the predetermined operations in the circuit arrangement nodes signal-classwise.

2. A method for signal processing in a receiver and/or transmitter of a radio system, **characterized** by comprising:

determining (402) for different circuit arrangement nodes at least one operation to execute and selecting a modification level from the circuit arrangement, merging the selected modification level nodes and deleting irrelevant nodes and links between the nodes and/or adding new links,

determining (404) one or more division criteria for signal classes for dividing the signals or signal components,

dividing (406) at least one of the signals or signal components according to the one or more division criteria for signal classes,

executing (408) the predetermined operations in the circuit arrangement nodes signal-classwise.

- 3. A method as claimed in claim 1 or 2, c haracterized in that the circuit arrangement is at least substantially in accordance with a combined tree structure such that at least one tree branch performs transmitter tasks and at least one second branch performs receiver tasks, and in which circuit arrangement one or more nodes of different branches is connected in a predetermined manner.
- 4. A method as claimed in claim 1 or 2, **c h a r a c t e r i z e d** in that the circuit arrangement is at least substantially in accordance with a centralized loop such that at least two subtrees are connected to the loop, of which subtrees at least one subtree performs the tasks of radio-frequency parts and at least one second subtree performs the tasks of baseband parts.

10

15

20

25

35

- 5. A method as claimed in claim 1 or 2, **c h a r a c t e r i z e d** in that the signals or the signal components transfer packet-form data and the signal classes are indicated in the packet header.
- 6. A method as claimed in claim 1 or 2, characterized in that the nodes perform the tasks of the radio-frequency parts or the baseband parts.
- 7. A method as claimed in claim 1 or 2, c h a racterized in that the circuit arrangement enables transfer of feedback information.
- 8. A method as claimed in claim 1 or 2, characterized in that signals to be modulated in different manners in one or more baseband nodes are divided into different signal classes.
- 9. A method as claimed in claim 1 or 2, **c h a r a c t e r i z e d** in that from the nodes it is possible to transmit data to one node (unicast) or a plurality of nodes (multicast or broadcast).
- 10. A method as claimed in claim 1 or 2, **characterized** in that network traffic load is monitored signal-classwise.
- 11. A method as claimed in claim 1 or 2, **characterized** in that the signal classes constitute a hierarchic signal class system, which class system comprises one or more levels.
- 12. A method as claimed in claim 1 or 2, **characterized** in that inter-node links have a maximum capacity, within which the number and type of the transmitted signal classes can be altered.
- 13. A method as claimed in claim 1 or 2, **characterized** in that the quality class is taken into account when the signal is clipped.
- 14. A method as claimed in claim 1 or 2, **characterized** in that the signal power is measured quality-classwise.
- 15. A method as claimed in claim 1 or 2, **characterized** in that the signals having different requirements for modulation accuracy are divided into different signal classes.
- 16. A method as claimed in claim 1 or 2, **characterized** in that the signals are divided into different signal classes after spatial, temporal and/or frequency-level preprocessing.
 - 17. A method as claimed in claim 1 or 2, **characterized** in that the signals are divided into different signal classes after interference cancellation preprocessing.

10

15

20

30

18. A circuit arrangement for signal processing in a receiver and/or transmitter of a radio system,

characterized in that

circuit arrangement nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) are arranged to perform at least one operation,

the circuit arrangement comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing at least one of the signals or signal components according to one or more predetermined division criteria for signal classes.

the circuit arrangement comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) for performing predetermined operations signal-classwise.

- 19. A circuit arrangement as claimed in claim 18, **character**-**ized** in that the circuit arrangement is at least substantially in accordance with a combined tree structure such that at least one tree branch performs transmitter tasks and at least one second branch performs receiver tasks, and in which circuit arrangement one or more nodes of different branches is connected in a predetermined manner.
- 20. A circuit arrangement as claimed in claim 18, **character**ized in that the circuit arrangement is at least substantially in accordance with a centralized loop such that at least two subtrees are connected to the loop, of which subtrees at least one subtree performs the tasks of radiofrequency parts and at least one second subtree performs the tasks of baseband parts.
- 21. A circuit arrangement as claimed in claim 18, **c** h a r a c t e r i z e d in that the signals or the signal components transfer packet-form data and the signal classes are indicated in the packet header.
 - 22. A circuit arrangement as claimed in claim 18, **character**-ized in that nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) perform the tasks of radio-frequency parts or baseband parts.
 - 23. A circuit arrangement as claimed in claim 18, characterized in that the circuit arrangement enables transfer of feedback information.
- 24. A circuit arrangement as claimed in claim 18, c h a r a c t e r i z e d in that the circuit arrangement comprises means (608, 614) for dividing
 35 the signals to be modulated in different manners into different signal classes.

10

15

20

25

- 25. A circuit arrangement as claimed in claim 18, **character- ized** in that from nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322)
 data can be transmitted to one node (unicast) or a plurality of nodes (multicast or broadcast).
- 26. A circuit arrangement as claimed in claim 18, **character**-ized in that network traffic load is monitored signal-classwise.
- 27. A circuit arrangement as claimed in claim 18, characterized in that the signal classes constitute a hierarchic signal class system, which class system comprises one or more levels.
- 28. A circuit arrangement as claimed in claim 18, **character**-ized in that links between nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) have a maximum transfer capacity, within which the number and type of the transferred signal classes can be altered.
- 29. A circuit arrangement as claimed in claim 18, characterized in that the quality class is taken into account when the signal is clipped.
- 30. A circuit arrangement as claimed in claim 18, characterized in that the signal power is measured quality-classwise.
- 31. A circuit arrangement as claimed in claim 18, **character**-ized in that the circuit arrangement also comprises a control unit (534), which controls the division into signal classes.
- 32. A circuit arrangement as claimed in claim 18, **character**-ized in that the circuit arrangement comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing signals having different requirements for modulation accuracy into different signal classes.
- 33. A circuit arrangement as claimed in claim 18, **character**-ized in that the circuit arrangement comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing signals into different signal classes after spatial, temporal and/or frequency-level preprocessing.
- 34. A circuit arrangement as claimed in claim 18, **character**30 **ized** in that the circuit arrangement comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing signals into different signal classes after interference cancellation preprocessing.

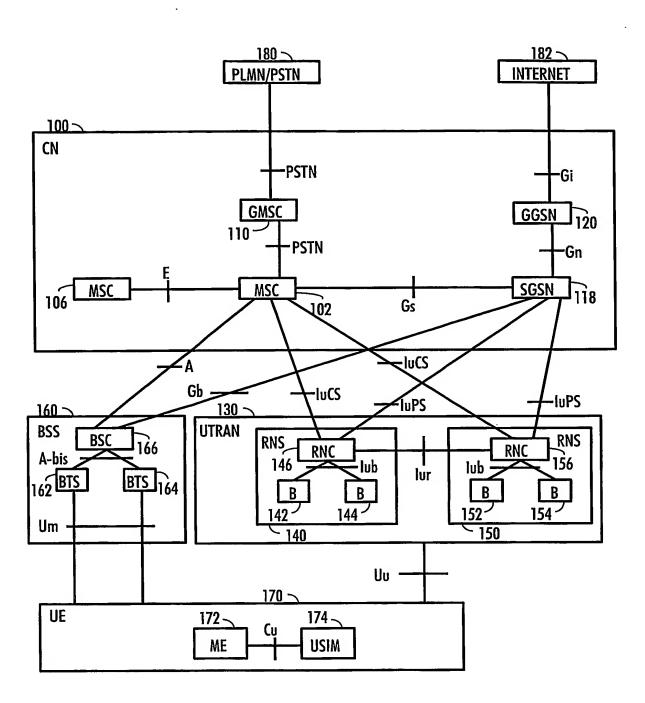
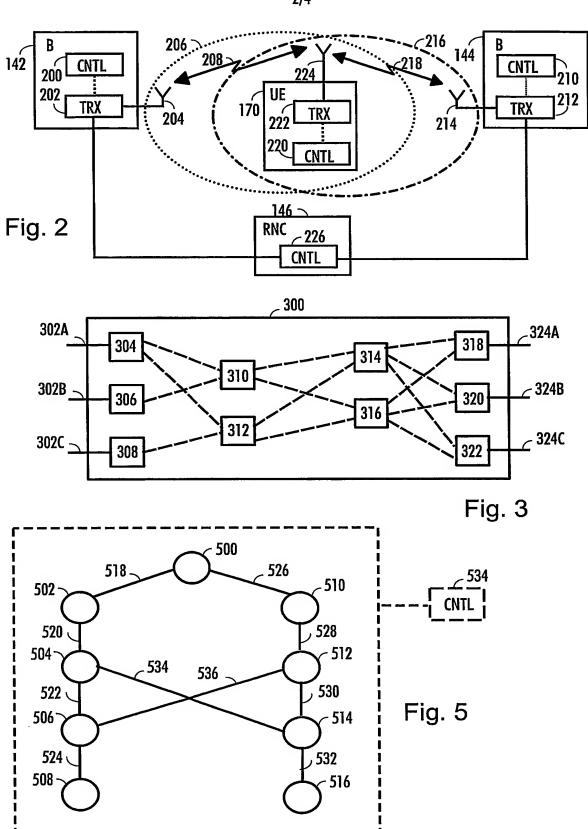


Fig. 1



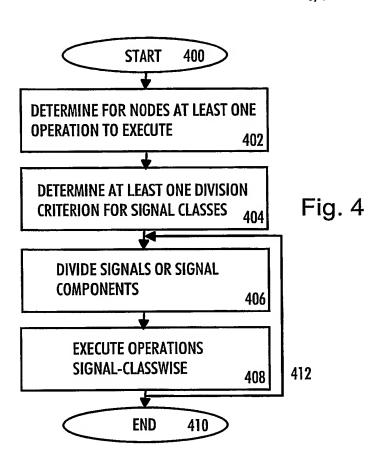
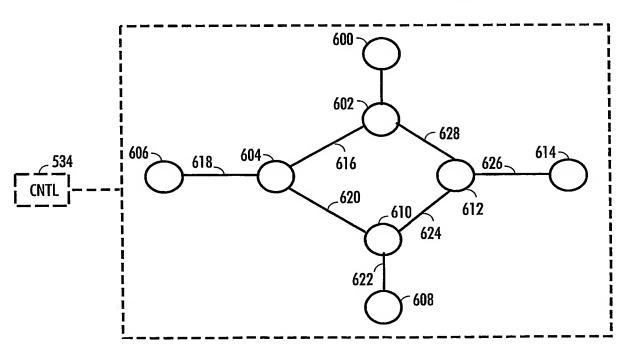


Fig. 6



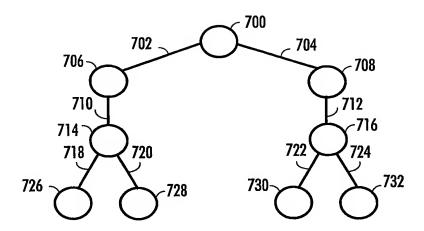


Fig. 7A

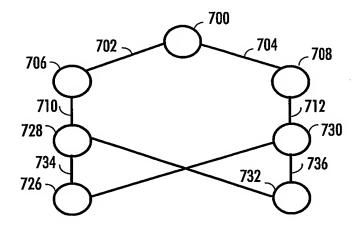


Fig. 7B



l application No. Intern PCT/FI 03/00530

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H04B 1/16, H04B 1/40
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H04B, H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-INTERNAL, WPI DATA, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|-----------|----------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| X | US 6240282 B1 (KLEIDER, J.E. ET AL), 29 May 2001 (29.05.01), column 2, line 47 - line 63; column 19, line 8 - column 20, line 40 | 1-34 |
| | | |
| X | US 5651030 A (WONG, C-P. ET AL), 22 July 1997 (22.07.97), column 1, line 36 - line 56; column 6, line 11 - line 25, claims | 1-34 |
| ļ | | |
| A | WO 0251044 A1 (TELEFONAKTIEBOLAGET LM ERICSSON (PUBL)), 27 June 2002 (27.06.02), page 2, line 15 - page 4, line 13 | 1-34 |
| | | |
| 1 | | |
| | | · |

| Ш | ruriner documents are listed in the continuation of Box | . C. | X See patent family annex. |
|-----------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| * "A" | Special categories of cited documents: document defining the general state of the art which is not considered to be of particular relevance | "T" | later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention |
| "E" "L" "O" "P" | earlier application or patent but published on or after the international filing date document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) document referring to an oral disclosure, use, exhibition or other means document published prior to the international filing date but later than the priority date claimed | "X" "Y" | |
| | e of the actual completion of the international search Sept 2003 | Date | of mailing of the international search report 1 8 -09- 2003 |
| Nan Swe | ne and mailing address of the ISA/ edish Patent Office 5055, S-102 42 STOCKHOLM | | rized officer |

Fredrik Blomqvist /OGU

+46 8 782 25 00

Telephone No.

Facsimile No. +46 8 666 02 86

INTERNATION SEARCH REPORT Information on family members

26/07/03

| Internal application No. | _ |
|--------------------------|---|
| PCT/F1 03/00530 | |

| | | Publication date | | Patent family member(s) | Publication date | |
|----|---------|---------------------|----------|----------------------------|----------------------------------------|----------------------------------|
| US | 6240282 | B1 | 29/05/01 | NONE | | |
| US | 5651030 | A | 22/07/97 | EP JP WO | 0538456 A 5508291 T 9220173 A | 28/04/93 18/11/93 12/11/92 |
| ψO | 0251044 | A1 | 27/06/02 | AU Ep Us | 3722602 A 1221777 A 2002137485 A | 01/07/02 10/07/02 26/09/02 |

Form PCT/ISA/210 (patent family annex) (July 1998)